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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/405,848	09/27/1999	TOSHIO NORITA	48864-021	9972

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MCDERMOTT WILL & EMERY LLP
600 13TH STREET, N.W.
WASHINGTON, DC 20005-3096

EXAMINER

AGGARWAL, YOGESH K

ART UNIT	PAPER NUMBER
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2622

DATE MAILED: 06/16/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/405,848	Applicant(s) NORITA ET AL.	
	Examiner Yogesh K. Aggarwal	Art Unit 2622	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 07 February 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final. 7
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 11-15 and 24-27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 15 is/are allowed.
- 6) ☒ Claim(s) 11-14 and 24-27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>01/04/2006</u> | 6) <input type="checkbox"/> Other: _____ |

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 02/07/2006 has been entered.

Response to Arguments

2. Applicant's arguments with respect to claims 11-14, 24-27 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 11 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art in view of Ishizaki et al. (US Patent # 5,182,658).

[Claim 11]

Applicant's own admitted prior art teaches a three-dimensional input apparatus comprising a projector for irradiating a detection light beam on an object (Page 1 lines 26-30), a scanning mechanism for scanning said object by deflecting the direction of irradiation of said detection light beam (Page 2 lines 1-19), an image sensing device with an image sensing surface including

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a plurality of two-dimensionally arranged light-receiving elements, for receiving the detection light beam reflected on said object (Page 3 lines 15-18).

Applicant's admitted prior art fails to teach a controller for controlling the electric charge accumulation time of plurality of said light-receiving elements such that a plurality of types of outputs with different electric charge accumulation times are produced by each of said light receiving elements, discriminating whether at least one of said plurality of types of output signals is saturated for each one of the two-dimensionally arranged light-receiving elements and selecting said non-saturated signals among said plurality of types of output signals for each one of the plurality of two-dimensionally arranged light-receiving elements based on the result of the discrimination.

However Ishizaki teaches a CPU 18 for controlling the electric charge accumulation time of plurality of said light-receiving elements such that a plurality of types of outputs with different electric charge accumulation times are produced by each of said light receiving elements (e.g. V_{max} and V_{min} shown in figure 4 corresponding to different accumulation times),

discriminating whether at least one of said plurality of types of output signals is saturated for each one of the two-dimensionally arranged light-receiving elements (figure 5, col. 5 lines 19-28, box 303)

and selecting said non-saturated signals among said plurality of types of output signals for each one of the plurality of two-dimensionally arranged light-receiving elements based on the result of the discrimination (output NO LOOP of the box 303 represents signals that are unsaturated).

Therefore taking the combined teachings of Applicant's admitted prior art and Ishizaki, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have a controller for controlling the electric charge accumulation time of plurality of said light-receiving elements such that a plurality of types of outputs with different electric charge accumulation times are produced by each of said light receiving elements, discriminating whether at least one of said plurality of types of output signals is saturated for each one of the two-dimensionally arranged light-receiving elements and selecting said non-saturated signals among said plurality of types of output signals for each one of the plurality of two-dimensionally arranged light-receiving elements based on the result of the discrimination in order to reduce the accumulation time for the object contrast and to optimally control the accumulation time thereby achieving a better feeling for the operation of the camera.

[Claim 24]

This is a method claim corresponding to apparatus claim 11. Therefore it has been analyzed and rejected based upon the apparatus claim 11.

5. Claims 12 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art in view of Ishizaki et al. (US Patent # 5,182,658) as applied to claim 11 above in further view of Kazama et al. (US Patent # 5,883,668).

[Claim 12]

Applicant's admitted prior art in view of Ishizaki fail to teach "wherein said controller controls said image sensing device so as to output a signal corresponding to the accumulated electric charge upon lapse of a first accumulation time and continue to accumulate electric charge while maintaining said accumulated electric charge until a second charge accumulation time".

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However Kazama et al. teaches a non-destructive readout technique in which there is a mix of pixels that were read before the update and pixels that were not read before the update which means the former pixels have underwent the reset operation and that the latter pixels have accumulated charge for a long period of time without undergoing the reset operation (col. 9 lines 23-37) and is read as outputting a signal corresponding to the accumulated electric charge upon lapse of a first accumulation time and continue to accumulate electric charge while maintaining said accumulated electric charge until a second charge accumulation time. Therefore taking the combined teachings of Applicant's admitted prior art, Ishizaki and Kazama, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have a controller that controls said image sensing device so as to output a signal corresponding to the accumulated electric charge upon lapse of a first accumulation time and continue to accumulate electric charge while maintaining said accumulated electric charge until a second charge accumulation time. The benefit of doing so would be so that a non-destructive readout operation can be performed in which only the pixels from which signals have been read are reset to drain accumulated charge as taught in Kazama (col. 9 lines 25-28).

[Claim 25]

This is a method claim corresponding to apparatus claim 12. Therefore it has been analyzed and rejected based upon the apparatus claim 12.

6. Claims 13 and 26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art, Ishizaki et al. (US Patent # 5,182,658), Kazama et al. (US Patent # 5,883,668) as applied to claim 12 above in further view of Kusaka et al. (US Patent # 5,589,909).

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[Claim 13]

Applicant's admitted prior art, Ishizaki and Kazama fail to teach, "wherein said controller selects among said non-saturated signals one having a long electric charge accumulation time".

However Kusaka et al. teaches that if the intensity of the target object is low (read as non-saturated signals) then signals with long charge accumulation are selected (col. 10 lines 57-67).

Therefore taking the combined teachings of Applicant's admitted prior art, Ishizaki Kazama and Kusaka, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have a controller that selects among said non-saturated signals one having a long electric charge accumulation time. The benefit of doing so would be so that conditions related to the intensity of light from the target object to be photographed can also be detected as taught in Kusaka (col. 10 lines 60-62).

[Claim 26]

This is a method claim corresponding to apparatus claim 13. Therefore it has been analyzed and rejected based upon the apparatus claim 13.

7. Claims 14 and 27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art in view of Ishizaki et al. (US Patent # 5,182,658) as applied to claim 11 above in further view of Kusaka et al. (US Patent # 5,589,909).

[Claim 14]

Applicant's admitted prior art in view of Ishizaki fail to teach, "wherein said controller selects among said non-saturated signals one having a long electric charge accumulation time".

However Kusaka et al. teaches that if the intensity of the target object is low (read as non-saturated signals) then signals with long charge accumulation are selected (col. 10 lines 57-67).

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Therefore taking the combined teachings of Applicant's admitted prior art, Ishizaki and Kusaka, it would have been obvious to one skilled in the art at the time of the invention to have been motivated to have a controller that selects among said non-saturated signals one having a long electric charge accumulation time. The benefit of doing so would be so that conditions related to the intensity of light from the target object to be photographed can also be detected as taught in Kusaka (col. 10 lines 60-62).

[Claim 27]

This is a method claim corresponding to apparatus claim 14. Therefore it has been analyzed and rejected based upon the apparatus claim 11.

Allowable Subject Matter

8. Claim 15 is allowed.

9. The following is a statement of reasons for the indication of allowable subject matter:

The prior art fails to suggest or teach a controller for controlling said image sensing device so as to output a first signal due to a first electric charge accumulation time and a second signal due to a second electric charge accumulation time equal to a predetermined multiple of said first signal during the electric charge accumulation of said image sensing device; and a selecting circuit for selecting said second signal in the case where said second signal has not been saturated and using selecting a signal of a size equal to said predetermined multiple of said first signal in the case where said second signal has been saturated; and a processor for performing calculations using the selected signal, said selecting circuit including: a first switch, a second switch, a memory, a comparator and an integrator wherein said first switch receives the first and second signals

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outputs the first signal to the memory and outputs the second signal to the second switch and to the comparator, the integrator receives the first signal from the memory and outputs the signal of a size equal to said predetermined multiple of said first signal to the second switch, and the comparator compares the second signal to a reference saturation level and outputs a control signal to the second switch to output the second signal where the second signal has not been saturated and to output the signal of a size equal to said predetermined multiple where the second signal has been saturated.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yogesh K. Aggarwal whose telephone number is (571) 272-7360. The examiner can normally be reached on M-F 9:00AM-5:30PM.

10. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Ometz can be reached on (571)-272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

11. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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YKA

June 8, 2006

A handwritten signature in black ink, appearing to read 'David Ometz', with a long horizontal line extending to the right.

DAVID OMETZ
SUPERVISORY PATENT EXAMINER